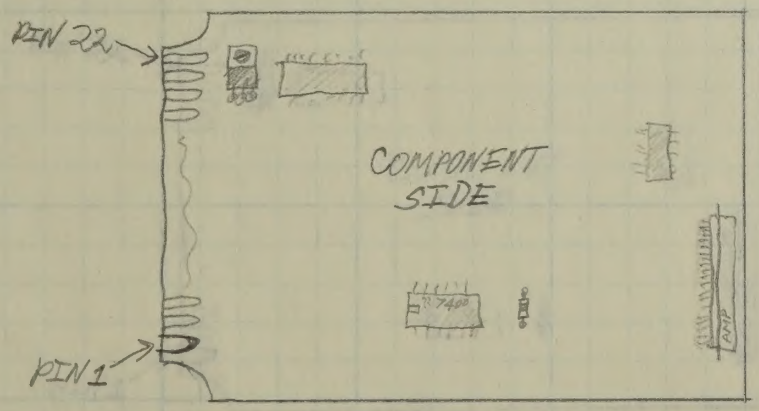
HP1676	17 JUNE, '82	WA6DWH

BUS PIN ASSIGNMENTS

PIN #	MNEMONIC	DESCRIPTION
22	+12	- +12 VDC FROM EXTERNAL SUPPLY
21	D7	BI-DIRECTIONAL DATA BUS
20	D6	
19	D5	
18	D4	
17	D3	
16	D2	
15	D1	
14	D0	
13	$\overline{RD}$	- PORT READ, ACTIVE LOW
12	$\overline{WR}$	- PORT WRITE, ACTIVE LOW
11	$\overline{INT}$	- MASKABLE INTERRUPT, ACTIVE LOW
10	$\overline{NMI}$	- NON-MASKABLE INTERRUPT, ACTIVE LOW
9		RESERVED FOR EXPANSION (LOGICALLY PLACED FOR EXPANSION OF ADDRESS BUS OR CPU CONTROL)
8		
7	A5	6 BIT ADDRESS BUS (FROM CPU)
6	A4	
5	A3	
4	A2	
3	A1	
2	A0	
1	⏚	- YOU'LL NEVER GUESS! HA HA HA HA!

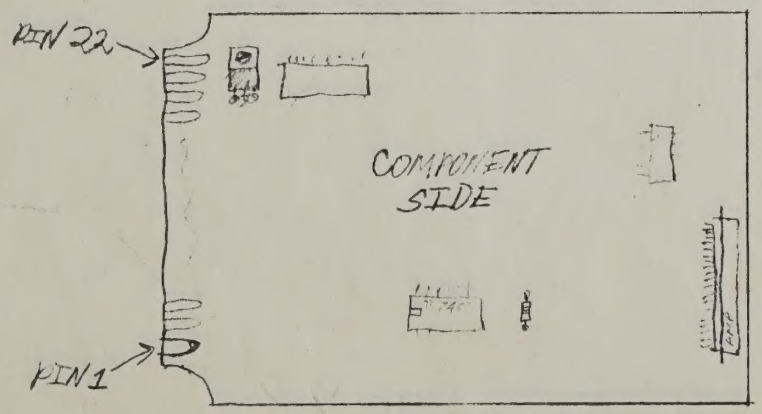


HP1676	17 June 82	WA6DWH

BUS PIN ASSIGNMENTS

Note Reversal on Vector Card

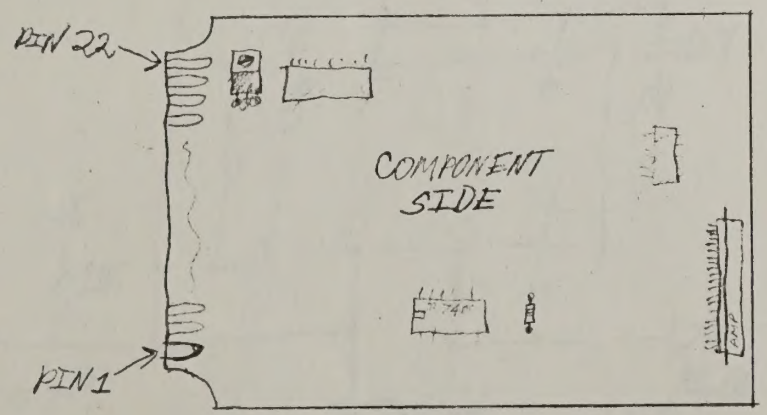
PIN #	MNEMONIC	DESCRIPTION
22	+12	– +12 VDC FROM EXTERNAL SUPPLY
21	D7	BI-DIRECTIONAL DATA BUS
20	D6	
19	D5	
18	D4	
17	D3	
16	D2	
15	D1	
14	D0	
13	$\overline{RD}$	– PORT READ, ACTIVE LOW
12	$\overline{WR}$	– PORT WRITE, ACTIVE LOW
11	$\overline{INT}$	– MASKABLE INTERRUPT, ACTIVE LOW
10	NMI	– NON-MASKABLE INTERRUPT, ACTIVE LOW!
9		RESERVED FOR EXPANSION (LOGICALLY PLACED FOR EXPANSION OF ADDRESS BUS OR CPU CONTROL)
8		
7	A5	6 BIT ADDRESS BUS (FROM CPU)
6	A4	
5	A3	
4	A2	
3	A1	
2	A0	
1	⏚	– YOU'LL NEVER GUESS! HA HA HA HA!



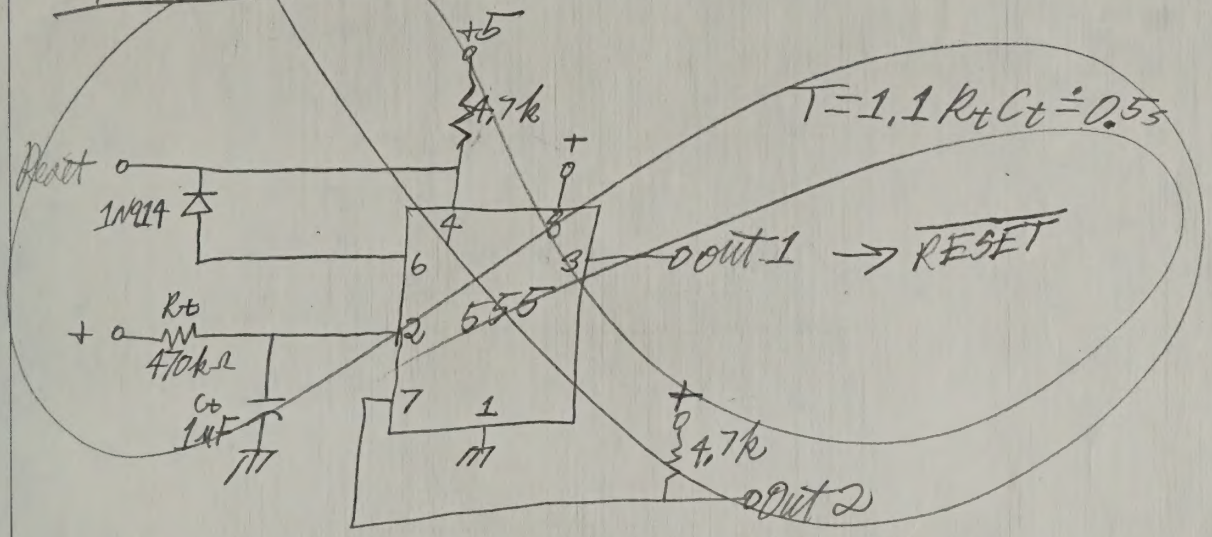
HP16	17 JUNE, '82	WA6DWH

BUS PIN ASSIGNMENTS

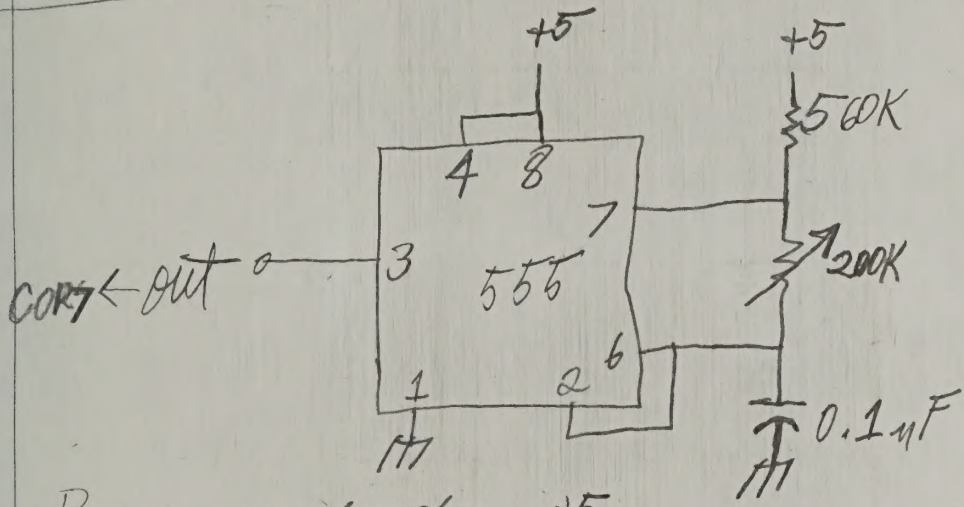
PIN #	MNEMONIC	DESCRIPTION
22	+12	– +12 VDC FROM EXTERNAL SUPPLY
21	D7	BI-DIRECTIONAL DATA BUS
20	D6	
19	D5	
18	D4	
17	D3	
16	D2	
15	D1	
14	D0	
13	$\overline{RD}$	– PORT READ, ACTIVE LOW
12	$\overline{WR}$	– PORT WRITE, ACTIVE LOW
11	$\overline{INT}$	– MASKABLE INTERRUPT, ACTIVE LOW
10	$\overline{NMI}$	– NON-MASKABLE INTERRUPT, ACTIVE LOW
9		RESERVED FOR EXPANSION (LOGICALLY PLACED FOR EXPANSION OF ADDRESS BUS OR CPU CONTROL)
8		
7	A5	6 BIT ADDRESS BUS (FROM CPU)
6	A4	
5	A3	
4	A2	
3	A1	
2	A0	
1	⏚	– YOU'LL NEVER GUESS! HA HA HA HA!



Power-up Reset



Interrupt Timer ... ON AUDIO CARD



Power-up reset